



# CPU Load

How to avoid common pitfalls + a new approach

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Bruges



**BOSCH** Continental



STELLANTIS

**TOYOTA** VOLKSWAGEN GROUP

# Agenda

- ▶ Introduction
- ▶ CPU load
- ▶ TASK load (new idea, proposal)
- ▶ Summary

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# What is 'load'?



rob-bier.medium.com

**Load**



Source: BSE-Galerie

**Overload**

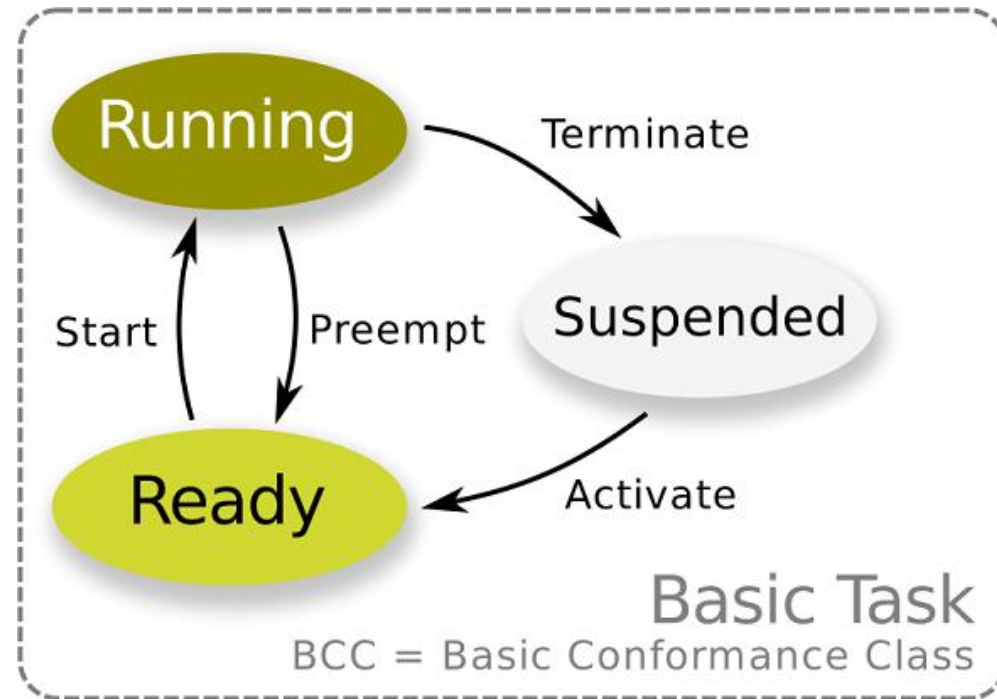


# Embedded Software Timing

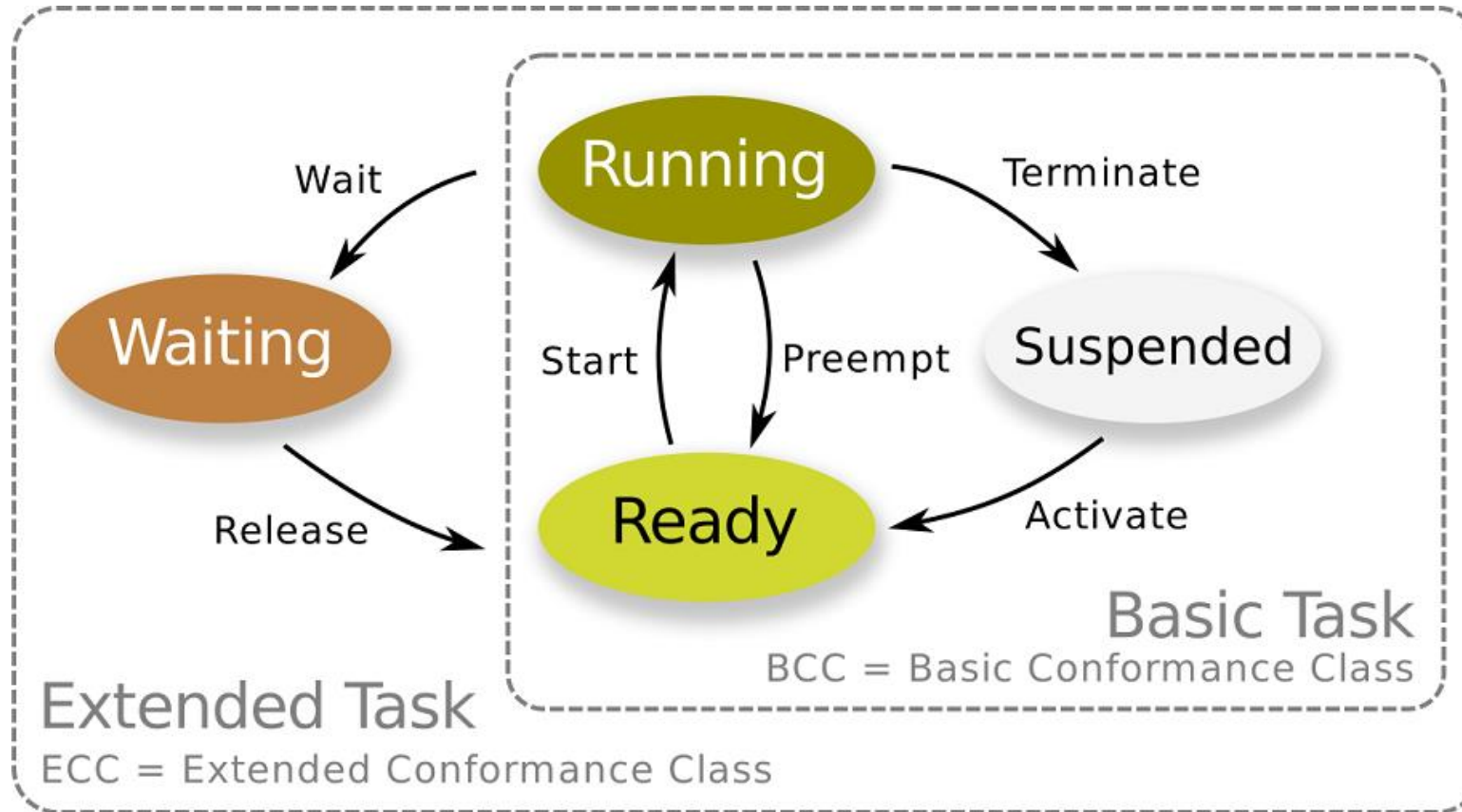
- Some of the following tables, pictures, etc. are taken from my book  
**Embedded Software Timing**
- Available in five languages:  
DE, EN, CN, KR, JP



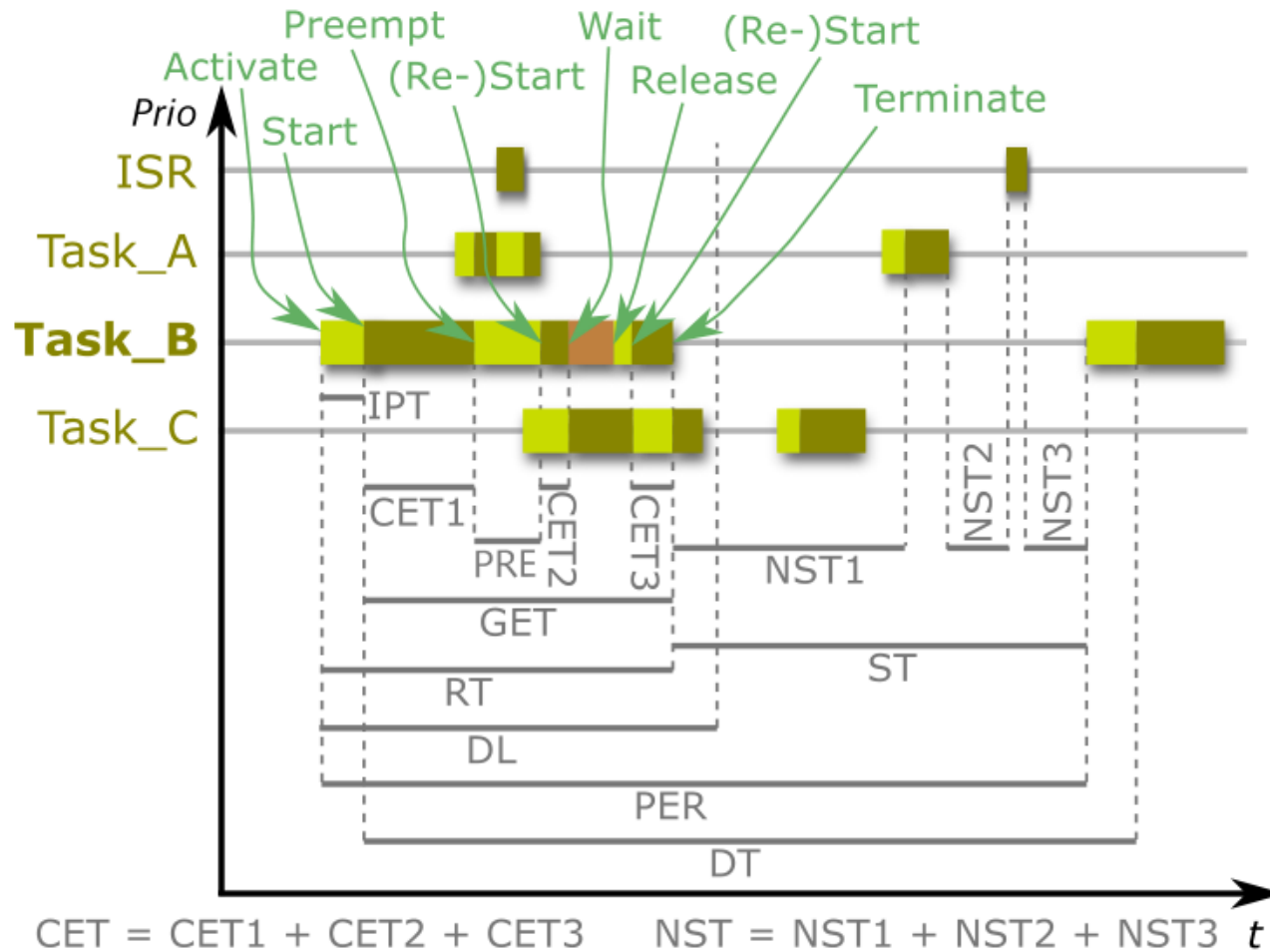
# AUTOSAR / OSEK Task States (BCC)



# AUTOSAR / OSEK Task States (BCC & ECC)



# Timing Parameters



- **IPT (Initial Pending Time)**  
Ready time before task starts
- **CET (Core Execution Time)**  
Time spent in running state, i.e. executing
- **GET (Gross Execution Time)**  
From start to termination (cf. pin toggle)
- **PRE (PREemption Time)**  
Sum of ready times without IPT
- **RT (Response Time)**  
cf. schedulability analysis; DL (DeadLine) = limit for RT
- **Period (PERiod)**  
time difference between two subsequent activations
- **DT (Delta Time)**  
time difference between two subsequent events of the same type; observed period; cf. jitter
- **ST (Slack Time)**  
duration of the 'gap'
- **NST (Net Slack Time)**  
headroom: time which could be added to CET

Relevant for  
**CPU load**  
calculation

Relevant for **TASK**  
**load** calculation



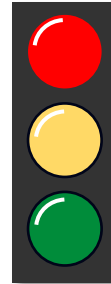
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# CPU Load: Manager's Darling

- CPU load – managers love it!
  - Complicated scheduling/timing world reduced to one single number
  - Even traffic lights can be derived

- **>85%** = **red**
- **70% .. 85%** = **yellow**
- **< 70%** = **green**



BTW: AUTOSAR  
does not 'know'  
CPU load  
although it is the  
most widely used  
timing parameter.

- Engineers: don't smile at this approach!
- Let this expectation guide the definition/calculation of the CPU load!

# CPU Load: Definition & Calculation

CPU load  $\rightarrow U = \frac{t_e}{t_o}$

$t_e$  in other words: time the CPU spent processing code other than idle code

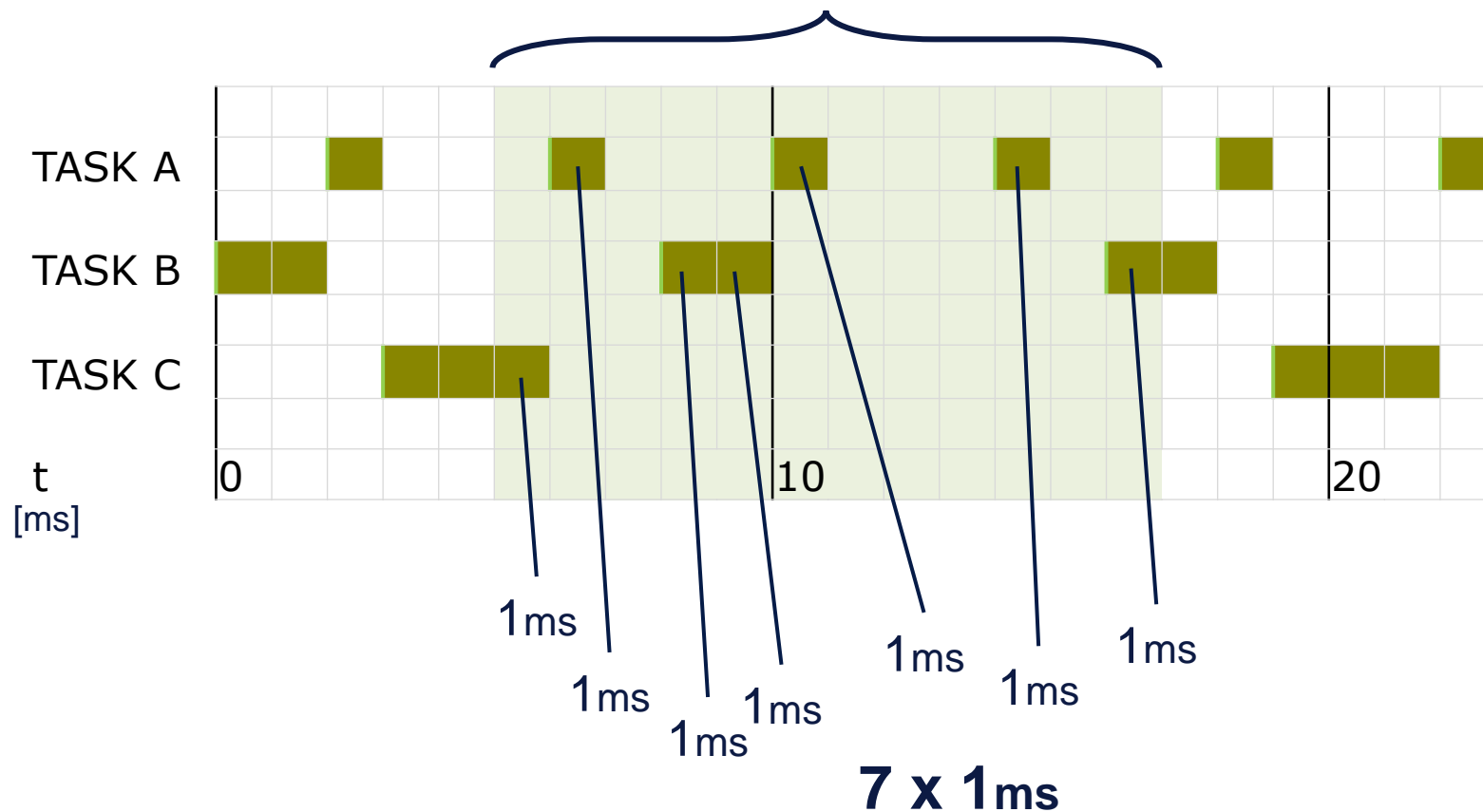
duration of the observation

$$t_e = \sum_{n=1}^N CET_n$$

$t_e$  is the sum of all CETs (TASKs and ISRs) that fall into the observation.

# CPU Load: Example

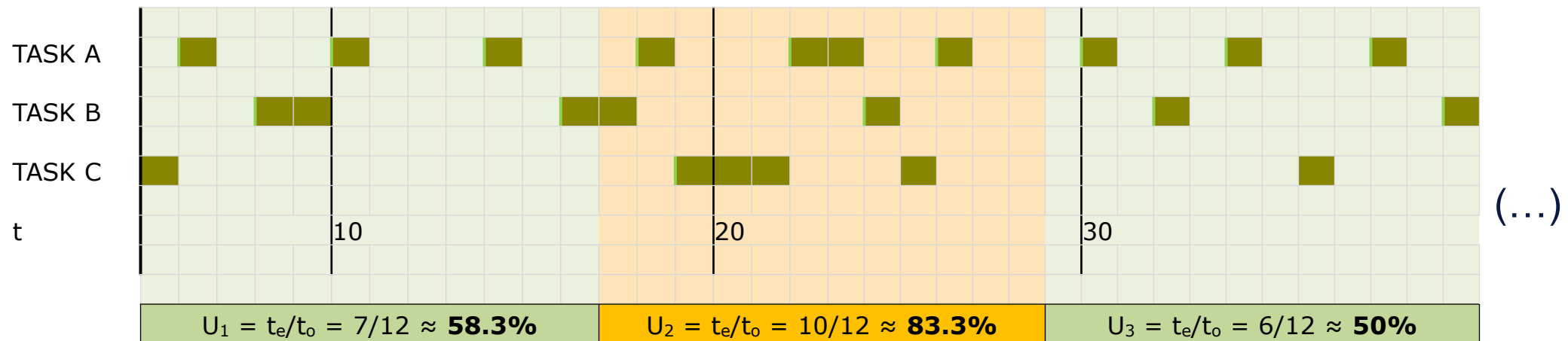
Observation frame  $t_o = 12\text{ms}$



$$U = t_e / t_o \\ = 7\text{ms} / 12\text{ms} \\ \approx 58.3\%$$

# 'CPU Load' Means 'Max. CPU Load'

The end of one observation frame marks the beginning of the next one.

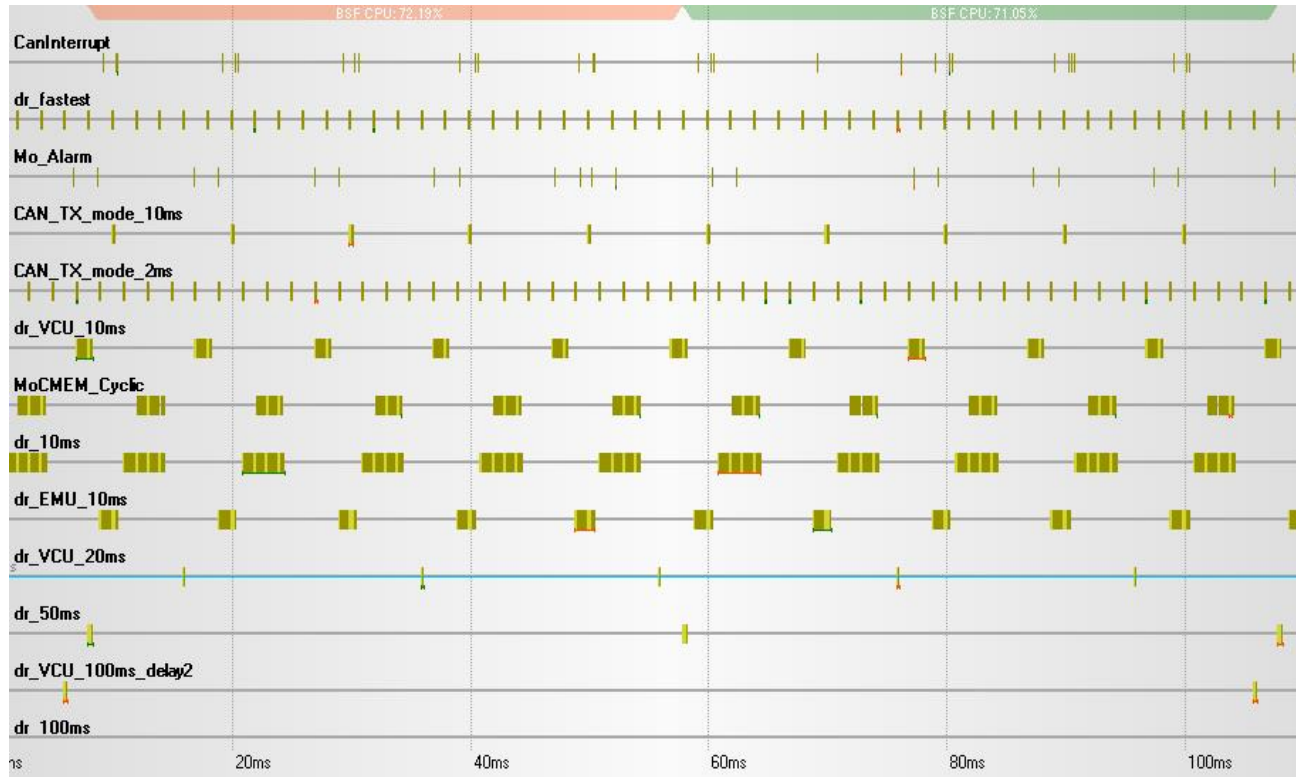


Which of the CPU loads is 'the' CPU load? Max? Average?

$$U = \max(U_1..U_n)$$



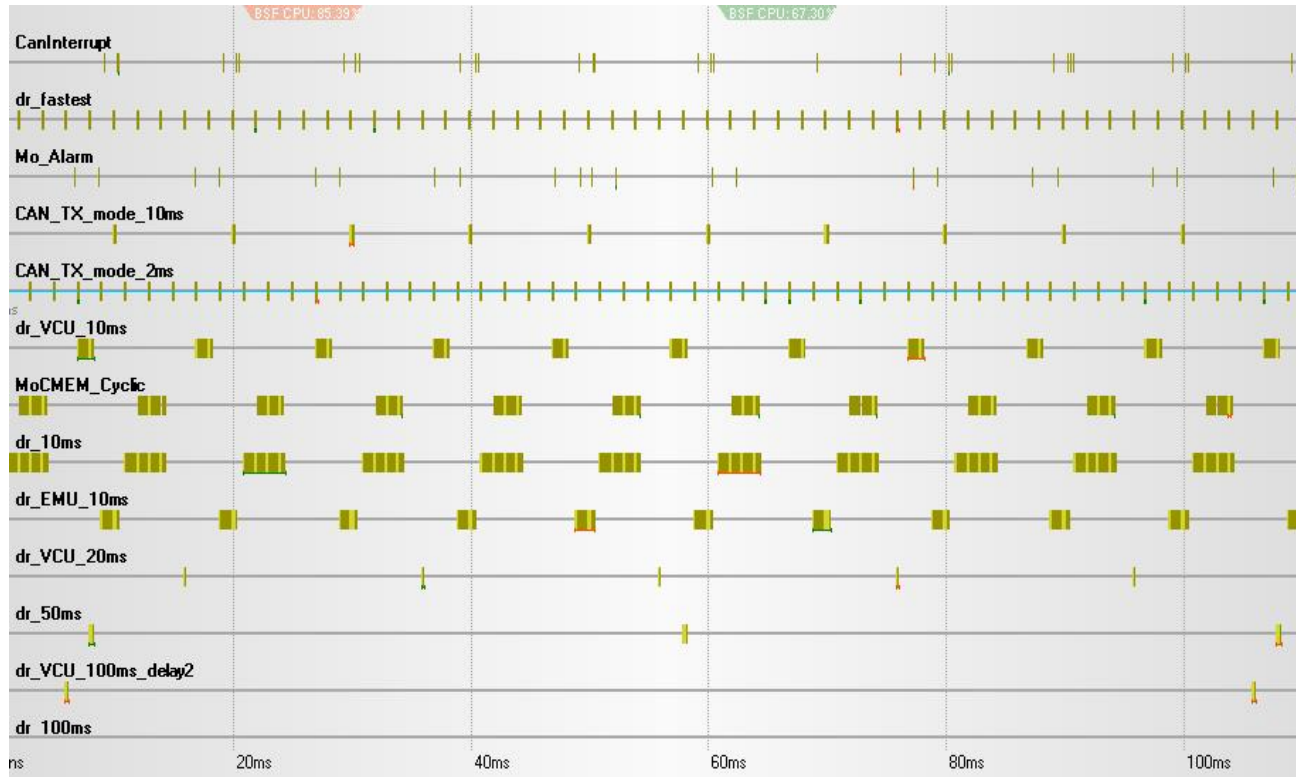
# CPU Load: **Big** Observation Frame



- BSF-event(\*) here: activation of **50ms** TASK
- The resulting max. CPU load is **72.19%**

\*: **Basic Scheduling Frame** event defining  $t_0$

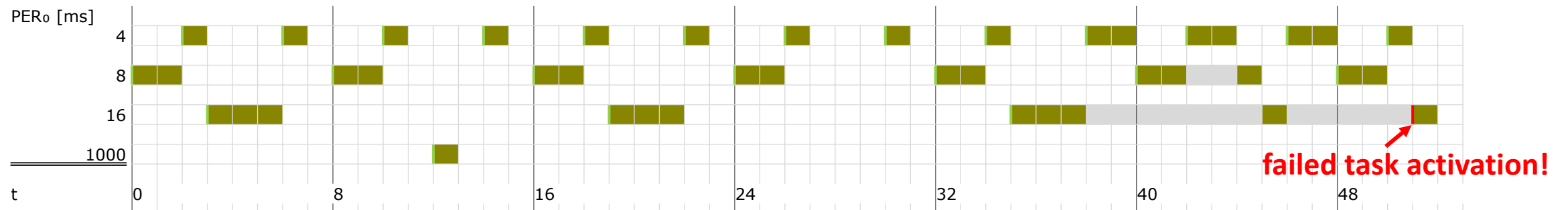
# CPU Load: **Small** Observation Frame



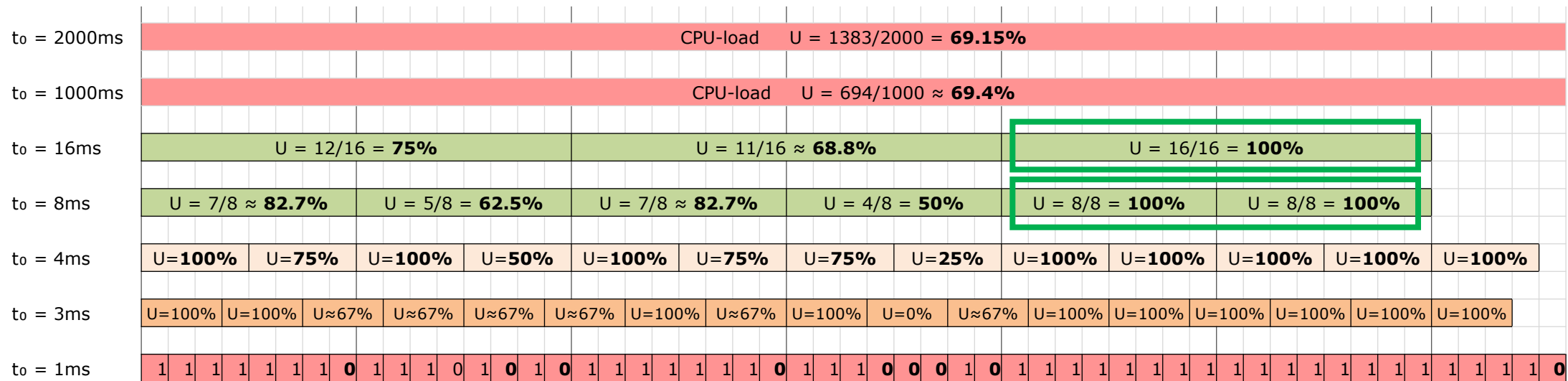
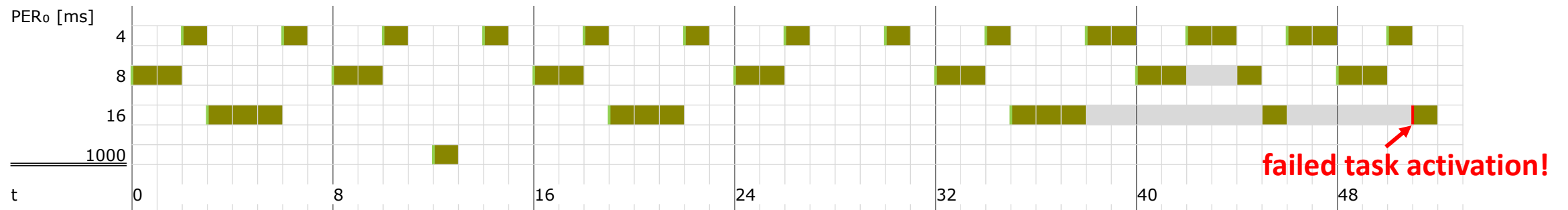
- BSF-event(\*) here: activation of **10ms** TASK
- The resulting max. CPU load is **85.39%**

\*: **Basic Scheduling Frame** event defining  $t_0$

# CPU Load: How to Select the Observation Frame



# CPU Load: How to Select the Observation Frame



# Which observation frame should you use?

- Unfortunately, there is no silver bullet.
- The 'right' observation frame depends on the project's schedule and requirements.
- However, with the skills you just gained, you should be able to pick the right  $t_o$ .
- The main control loop (if there is one) is typically a good starting point.
- $t_o$  will probably be in the range 10ms to 50ms.



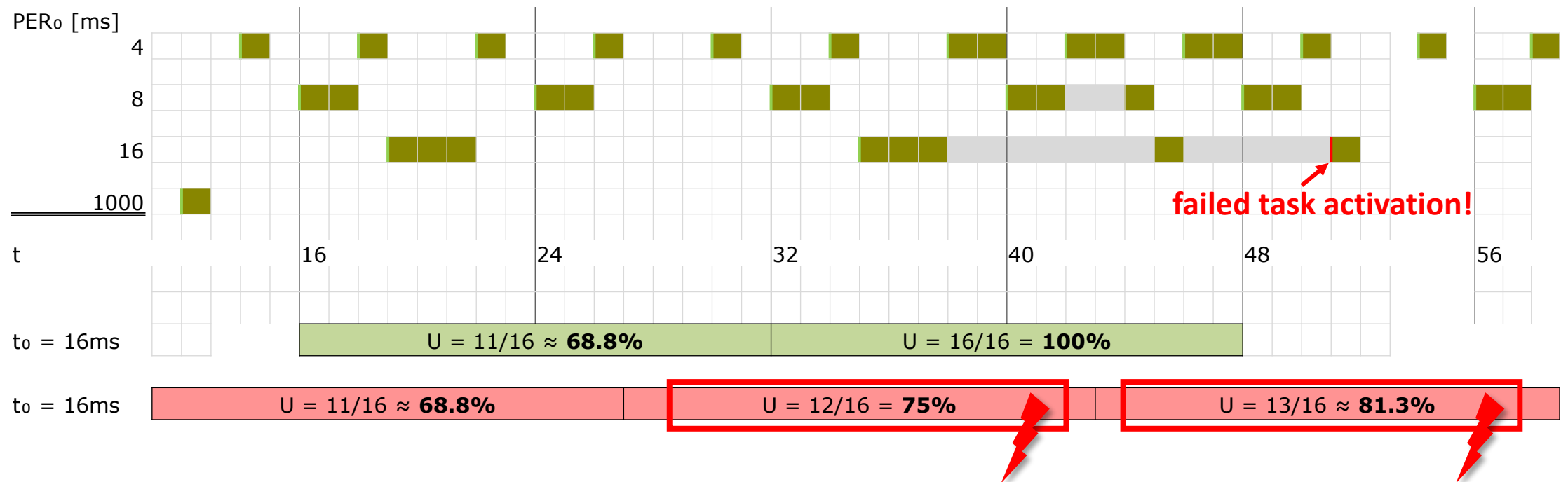


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# Are we there yet?

Not quite. Even with  $t_o = 16\text{ms}$ , there is a problem if we shift the observation frame a bit.



# Use a sliding window?

Cf. moving/sliding average vs. intermediate average

Index $i$	1	2	3	4	5	6	7	8	9	10	11	12
Value $x_i$	96	20	28	36	53	27	41	32	62	36	73	21
Overall average	44											
Intermediate average	45											
					38							
									48			
Moving average	45											
		34										
			36									
				39								
					38							
						41						
							43					
								51				
									48			

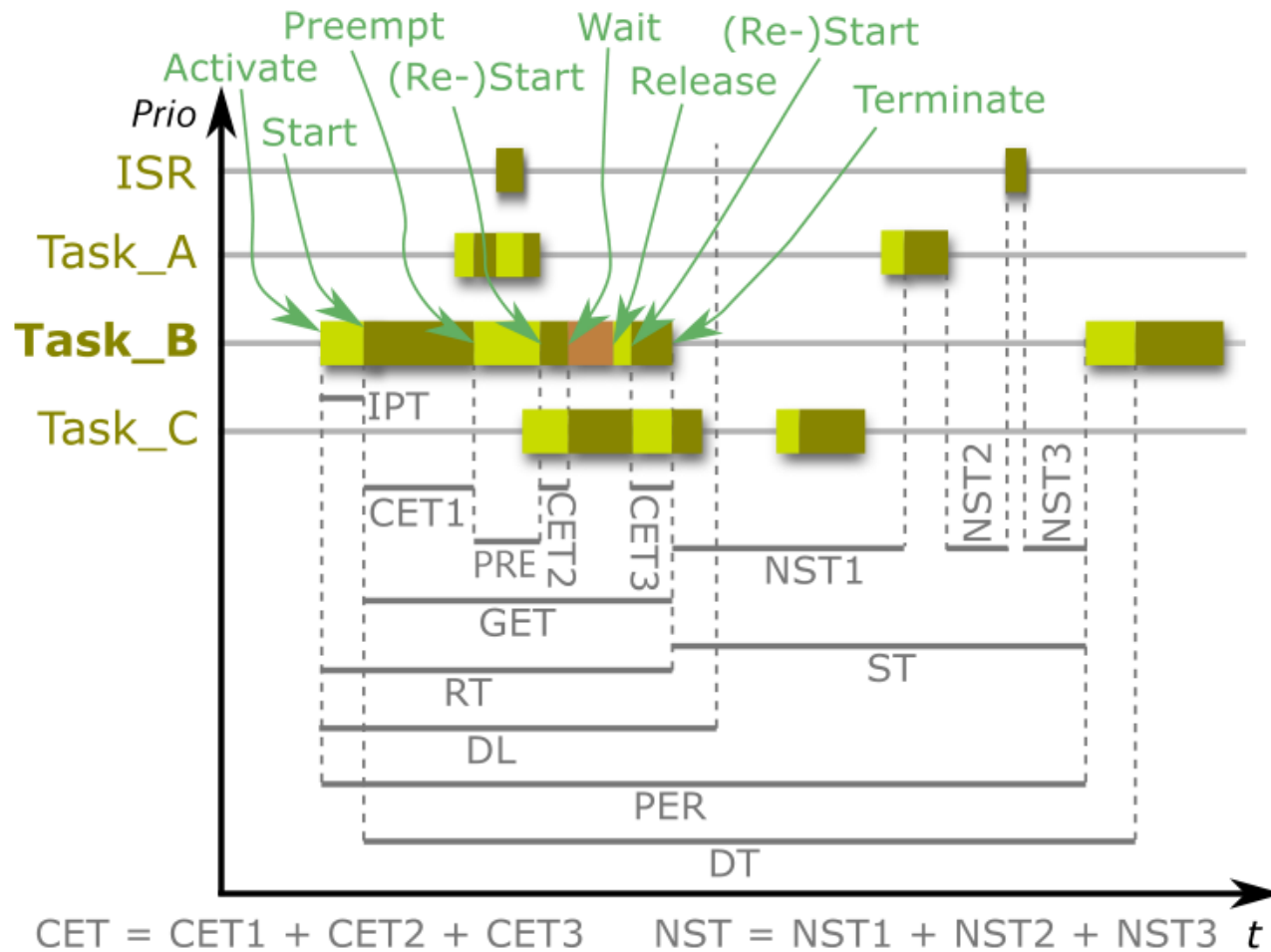
CPU load is always an average of the load  $L$  in infinitesimal time frames (idle loop is executed:  $L=0$ ; TASK, ISR, etc. is executed:  $L=1$ ).

Previously discussed approach (subsequent observation frames)

Possible alternative?

Too complicated, too costly to calculate efficiently

# Introduction of 'TASK load' → recap NST



- **IPT (Initial Pending Time)**  
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Relevant for **TASK load** calculation

# Introduction of 'TASK load' → the idea

- The NST of a task nicely indicates the 'head-room' of this task.
- If there is no more headroom (NST = 0), the task is 'overloaded'.
- The unit of the NST is s (or ms or μs or ns etc.). Not a good fit for 'load'.
- Could we use a relative NST? What would it be normalized with?
- For periodical tasks, we could say:

$$N_T = \frac{\overbrace{PER - NST}^{\text{Time 'used up' within period}}}{\underbrace{PER}_{\text{Period}}} = \underline{\underline{1 - \frac{NST}{PER}}}$$



# TASK load definition

- Each pair of subsequent occurrences of  $TASK_T$  results in an NST.
- The TASK load  $N_T$  of each  $TASK_T$  would obviously be the possible or observed *maximum* NST. Cf.  $\max(U_0 \dots U_n)$  discussed earlier.
- Obviously, there are as many  $N_T$  results as there are TASKs.
- Definition:

**The TASK load  $N$  of a system with  $q$  periodical TASKs is the maximum  $N_T$  of all tasks.**

$$N = \max(N_{T0} \dots N_{Tq})$$

- What about non-periodic TASKs and ISRs? I don't know.  
Need to think about it...

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# Summary

- People tend to think it is obvious what 'CPU load' means.
- Different definitions/assumptions/understandings lead to different results and, in the end, to **problems in real projects**.
- Suggestion: discuss and establish a *common understanding and definition*. Maybe

$$\text{Load} = \max(U, N)$$

- Whatever load definition you pick, *thinking* about this topic and deriving actions will very likely **improve the quality of embedded software!**



# Thank You!



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